

**School of Engineering and Computer Science**  
**ECE 234: Microprocessor Systems**  
**Master Syllabus**

<b>Catalog Data:</b>	<b>ECE 234: Microprocessor Systems;</b> 3 credits (2-3) Microprocessor system architecture, instruction sets, and interfacing; assembly language programming. Typically offered in Spring.
<b>Class Schedule:</b>	Two lectures hours per week, for one semester.
<b>Laboratory Schedule:</b>	One 3-hour lab session per week, for one semester.
<b>Prerequisites by Course:</b>	CS 251 or CS 261; ECE 214
<b>Prerequisites by Topic:</b>	<ol style="list-style-type: none"> <li>1. Ability to design, code, and debug computer programs written in an imperative programming language such as C.</li> <li>2. Ability to design and implement combinational logic circuits.</li> </ol>
<b>Typical Text:</b>	<i>Embedded Systems with ARM Cortex-M Microcontrollers in Assembly Language and C: Third Edition</i> Y. Zhu, E-Man Press 2017, ISBN 978-0-9826926-6-0.
<b>Course Coordinator:</b>	Dr. John Lynch
<b>Course Objectives:</b>	<p>Students taking this course will:</p> <ol style="list-style-type: none"> <li>1. Design, code and debug a program, using assembly language, which performs an iterative or recursive algorithm, such as sort or search, and which implements the function calling conventions of a high level language.</li> <li>2. Construct a hardware interface between a computer and an external device and program the computer to manipulate the device.</li> <li>3. Become familiar with the basic von Neumann stored program computer architecture as implemented in modern microprocessors.</li> </ol>
<b>Topics Covered:</b>	<ol style="list-style-type: none"> <li>1. Overview of transistors, gates and semiconductor technology</li> <li>2. Review of combinational and sequential logic</li> <li>3. The von Neumann stored program architecture</li> <li>4. Instruction set architectures</li> <li>5. Assembly language programming and interfacing with C</li> <li>6. Memory organization and addressing modes</li> <li>7. Interrupts and I/O architecture</li> <li>8. Bus structures and datapaths</li> </ol>
<b>Lab Experiments and Activities:</b>	<ol style="list-style-type: none"> <li>1. Introductory assembly language program</li> <li>2. Assembly language flow of control instructions and iteration</li> <li>3. Complex instructions, such as repetition, loops</li> <li>4. Interrupt handling</li> <li>5. Construct a hardware interface to an external device and write a program to control it</li> </ol>

<b>Course Outcomes:</b>	Students will be able to:		
	<b>Assessed for Student Outcomes</b>	2-b Apply design process to satisfy project requirements for microprocessor systems. 2-d Produce solutions using microprocessors that meet specified needs. 3-a Produce lab reports and/or research papers using appropriate formats and grammar with discipline-specific conventions.	
	<b>Other</b>	4-a. Evaluate the global, economic, environmental and societal impacts of microprocessors. 6-b. Use appropriate equipment and techniques to debug microprocessor systems. 7-a. Use resources effectively to learn new material not taught in class.	
<b>Relationship of Course to Program:</b>	Meets: Educational Objectives <u>1, 2, 3, 4</u> Student Outcomes <u>2, 3, 4, 6, 7</u>		
<b>Prepared by:</b>	Dr. John Lynch	<b>Date:</b>	03/2018; 2/21/18 (mb); 6/22/21 (mb) Jan. 4, 2010 revised :10/2011 Revised 02/12, revised 9/4/12, revised 09/2017